## Qualification Results Summary for LFCSP at STATS ChipPAC China Jiangyin

TEST	Specification	SAMPLE Size	RESULTS
Temperature Cycle (TC)*	JEDEC JESD22-A104	3 x 77	PASS
Solder Heat Resistance (SHR)*	JEDEC/IPC J-STD-020	3 x 11	PASS
Unbiased Highly Accelerated Stress Test (uHAST)*	JEDEC <i>JESD22-A118</i>	3 x 77	PASS
Electrostatic Discharge <i>Field</i> <i>Induced Charge Device Model</i>	JEDEC JESD22-C101	3/voltage	PASS ±750V

\* Preconditioned per JEDEC/IPC J-STD0020

## Automotive Qualification Results Summary for LFCSP\_SS at STATS ChipPAC China Jiangyin

TEST	SPECIFICATION	SAMPLE Size	RESULTS
Temperature Cycle (TC)*	JEDEC JESD22-A104	3 x 77	PASS
Solder Heat Resistance (SHR)*	JEDEC/IPC J-STD-020	3 x 11	PASS
Highly Accelerated Stress Test (HAST)*	JEDEC JESD22-A110	3 x 77	PASS
Unbiased Highly Accelerated Stress Test (uHAST)*	JEDEC JESD22-A118	3 x 77	PASS
High Temperature Storage (HTS)	JEDEC JESD22-A103	1 x 77	PASS
Electrostatic Discharge Field Induced Charge Device Model – All Pins	JEDEC JESD22-C101	3/voltage	PASS ±500V
Electrostatic Discharge Field Induced Charge Device Model – Corner Pins	JEDEC JESD22-C101	3/voltage	PASS ±750V

<sup>\*</sup> These samples were subjected to preconditioning (per J-STD-020 Level 3) prior to the start of the stress test. Level 3 preconditioning consists of the following: 1. Bake – 24 hours at 125°C; 2. Soak – unbiased soak for 192 hours at 30°C, 60%RH; 3. Reflow – three passes through a reflow oven with a peak temperature of 260°C. TC samples were subjected to wire-pull test after 500 cycles where results were within specification limits.

## Qualification Results Summary for Mini-LFCSP at STATS ChipPAC China Jiangyin

TEST	Specification	SAMPLE Size	RESULTS
Temperature Cycle (TC)*	JEDEC JESD22-A104	3 x 77	PASS
Solder Heat Resistance (SHR)*	JEDEC/IPC J-STD-020	3 x 11	PASS
Unbiased Highly Accelerated Stress Test (uHAST)*	JEDEC JESD22-A118	3 x 77	PASS
Electrostatic Discharge <i>Field</i> <i>Induced Charge Device Model</i> * Preconditioned per IEDEC/IPC I-STD0020	JEDEC JESD22-C101	3/voltage	PASS ±1250V

\* Preconditioned per JEDEC/IPC J-STD0020.